

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph bridging page 30, line 21 through page 31, line 3 of the specification with the following:

Receiver 350 also comprises a receive path and a feedback path. The receive path comprises antenna ~~355~~ 351, N Fast Fourier Transform (FFT) processors 355, mixer 360, N channel demodulators 365, and forward error correction (FEC)/ frame processing block 370. The feedback path comprises channel gain controller 375.

Please replace the paragraph bridging page 38, line 19 through page 39, line 6 of the specification with the following:

TDD frame 500 comprises a downlink portion containing preamble field 501, management field 502, and N modulation groups, including modulation group 503 (labeled Modulation Group 1), modulation group 504 (labeled Modulation Group 2), and modulation group 505 (labeled Modulation Group N). TDD frame 500 also comprises an uplink portion containing transmitter-transmitter guard (TTG) slot 506, 0 to N registration (REG) minislots ~~506~~ 507, 1 to N contention (CON) request minislots 508, N sub-burst slots, including sub-burst slot 509 (labeled

Sub-Burst 1) and sub-burst slot 510 (labeled Sub-Burst N), and receiver-transmitter guard (RTG) slot 511.